



WELCOME TO
E-Link

HVI ISO 9001 2015

The World's Source for High Voltage Test Equipment

MADE IN THE USA

High Voltage, Inc. • hvinc.com • p. 518.329.3275 • f. 518.329.3271 • 31 County Route 7A • Copake, NY 12516 USA

E-Link is HVI's feature packed remote control and report generation software package

The screenshot displays the E-Link software interface, which is divided into several functional panels:

- Left Panel:** A large 'RUN TEST' button with a lightning bolt icon and a 'WELCOME TO E-Link' banner.
- Top Center Panel:** A 'Test In Progress' status window showing a waveform graph and real-time data:
 - Voltage (kV): 22.256
 - Current (mA): 3.512
 - Capacitance (uF): 0.251
 - Resistance (MΩ): > 3,276
 - Tan Delta (E-3): 0.29
 - Step / Cycle: 4 / 10
 - Time: 28 mins 19 secs
- Bottom Center Panel:** A table of test steps with columns for Step, Voltage, Current, Capacitance, Resistance, Tan Delta, and TD Std. Dev.

Step	Voltage	Current	Capacitance	Resistance	Tan Delta	TD Std. Dev.
1	3.630	0.572	0.251	>3,276.7	0.22	0.01
2	7.277	1.148	0.251	>3,276.7	0.22	0.01
3	10.924	1.724	0.251	>3,276.7	0.24	0.01
4	22.257	3.512	0.251	>3,276.7	0.29	0.01
5	-	-	-	-	-	-
Mean	9.524	1.503	0.251	>3,276.7	0.24	-
Std. Dev.	6.063	0.957	0.000	0.0	0.02	-
- Right Panel:** A 'Graphing And Reporting' window. It includes a file browser on the left, a graph titled 'TD vs. Voltage by Steps' showing a downward trend, and a 'Report Items' section with 'Move >>' and 'Move <<' buttons. Below the graph is a table of test results:

Step	Voltage	Current	Capacitance	Resistance	Tan Delta	TD Std. Dev.
1	3.094	1.080	0.556	313.2	9.15	0.07
2	6.189	2.152	0.553	367.7	7.82	0.01
3	9.270	3.218	0.553	389.1	7.40	0.01
4	15.400	5.340	0.552	407.0	7.09	0.01
5	-	-	-	-	-	-
Mean	9.525	3.306	0.553	374.9	7.75	-
Std. Dev.	4.897	1.695	0.001	35.4	0.78	-

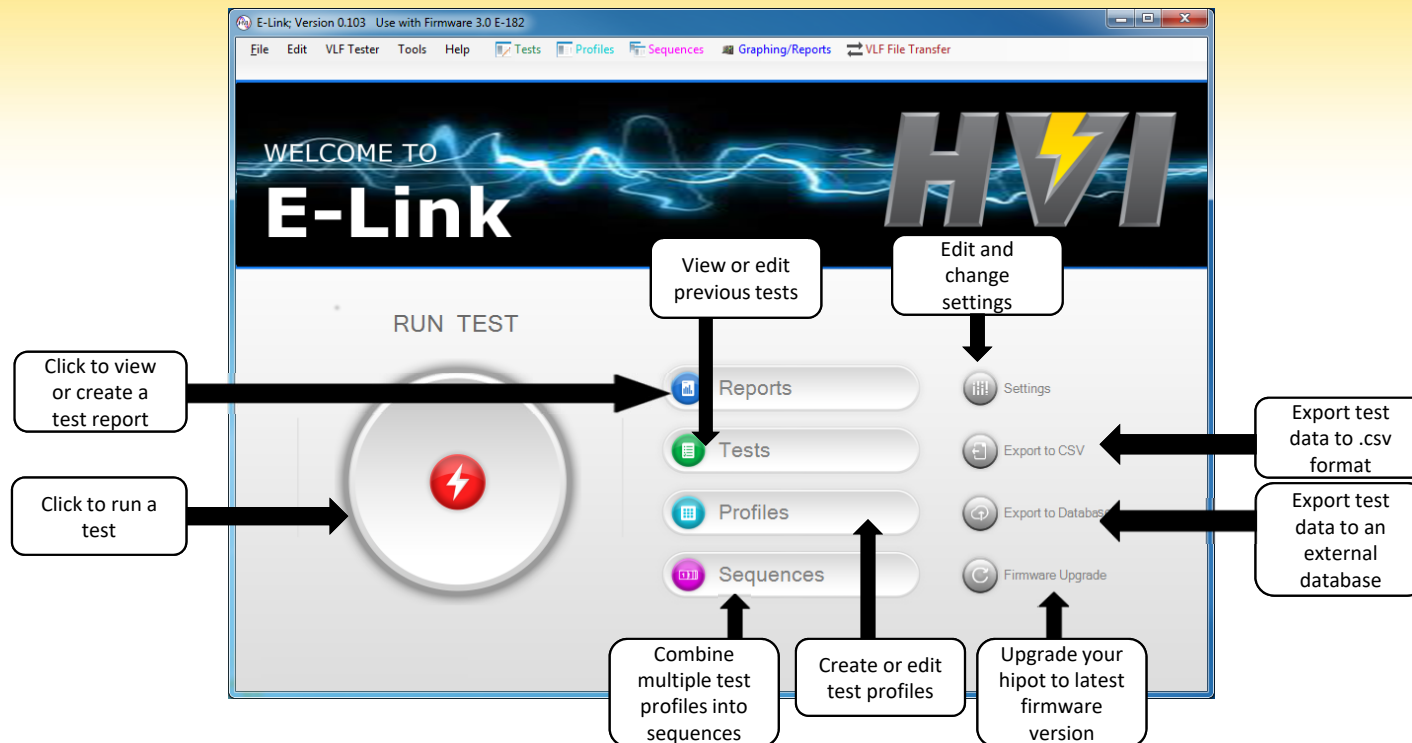
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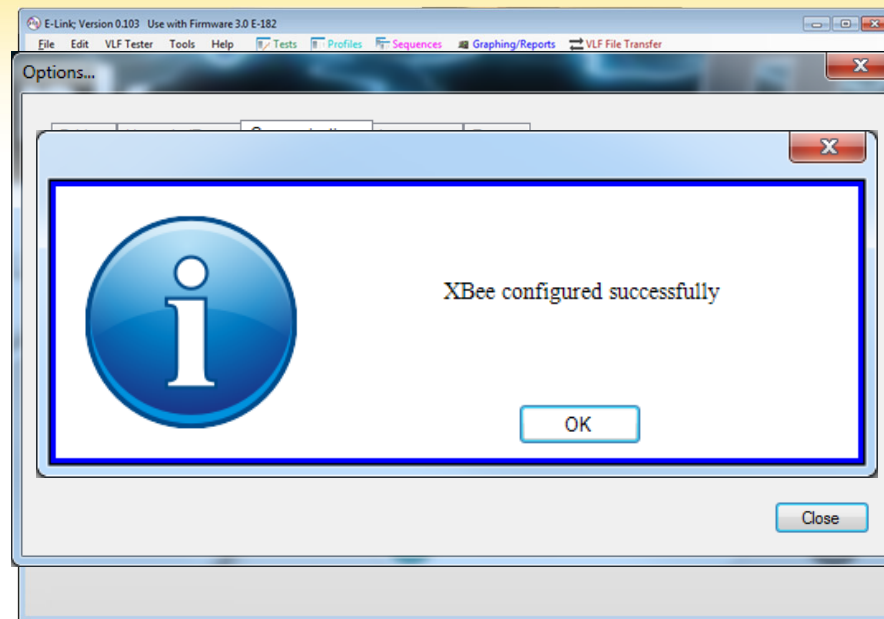
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The Home Screen



Connecting Wirelessly to VLF



Remote Operation of Hipot via XBee Wireless Protocol

The screenshot displays the E-Link software interface across three windows. The left window shows the 'WELCOME E-LINK' screen with a 'Click Run Test' callout. The middle window shows the test configuration for 'HIGH VOLTAGE INC. VLF-34E' with a 'Click Start for a second time to confirm action' callout. The right window shows the 'Test In Progress' screen with a 'Graph for analysis of TD, V, I, and Time' callout. Below the test data table, there are callouts for 'Live test data', 'Test data from each step', and 'Add identifying information about the system under test'. The bottom right window shows a 'TD vs. Steps' graph and a metadata form.

Test Data Table:

Step	Voltage (kV)	Current (mA)	Subsidence	Resistance	TD (kV)	TD (%)
1	3.630	0.572	0.251	>3.276.7	0.22	0.01
2	7.277	1.148	0.251	>3.276.7	0.22	0.01
3	10.924	1.724	0.251	>3.276.7	0.24	0.01
4	22.257	3.512	0.251	>3.276.7	0.29	0.01
5	-	-	-	-	-	-
Mean	9.524	1.503	0.251	>3.276.7	0.24	-
Std. Dev.	6.683	0.957	0.000	0.0	0.00	-

Custom Report Generation

Choose the location where the test data is stored

Choose files for the report

Choose what data for the report

Your test report has been created

Test Demonstration

Test Summary

File Name	Total Steps	Total Cycles	Total Duration	Tan Delta (E-3)	Capacitance (uF)	Resistance (MOhms)
20170621131241.hvc	4	68	00:11:20	0.517	0.250	> 3.276 M
20170621130302.hvc	3	54	00:09:00	0.337	0.251	> 3.276 M
20170425113621.hvc	4	84	00:14:00	8.115	0.552	374.904

Parameters - 20170621131241.hvc

Waveform: Sinewave Frequency: .1 (hz) Fault: Overload
 File Name: c:\20170621131241.hvc Phase: A
 Date/Time: 6/21/2017 1:19:18 PM

Location - 20170621131241.hvc

Circuit ID: Location:
 Sub-Station: Building: Project:

Location - 20170621130302.hvc

Circuit ID: Location:
 Sub-Station: Building: Project:

Step	Voltage	Current	Capacitance	Resistance	Tan Delta	TD Std. Dev.
1	3.094	1.080	0.556	313.2	9.15	0.07
2	6.189	2.152	0.55			0.1
3	9.270	3.218	0.55			0.1
4	15.400	5.340	0.55			
5	-	-	-			
Mean	9.525	3.306	0.553	374.9	7.75	
Std. Dev.	4.897	1.695	0.001	35.4	0.78	

Change the Test Report Title

The image shows two windows from the E-Link software. The left window is the main interface, titled 'E-Link; Version 0.103 Use with Firmware 3.0 E-182'. It features a 'WELCOME TO E-Link' banner, a 'RUN TEST' button with a lightning bolt icon, and a sidebar with icons for Reports, Tests, Profiles, and Sequences. An 'Options...' dialog box is open, showing the 'Folders' tab. The 'Title' field is set to '15kV VLF Withstand Test' and the 'Logo file' field is empty. The right window is a 'Print Preview' window showing a test report. A callout box points to the title '15kV VLF Withstand Test' with the text 'The test report has an updated title'. The report includes a table for 'Test Summary' and sections for 'Parameters', 'Location', and 'Cable'.

File Name	Total Steps	Total Cycles	Total Duration	Tan Delta (E-3)	Capacitance (uF)	Resistance (MOhm)
20190509140629.hvc	3	54	00:09:00	7.648	0.559	382.308

Parameters - 20190509140629.hvc
Waveform: Sinewave Frequency: .1 (hz) Fault: Overload
File Name: C:\20190509140629.hvc
Date/Time: 5/9/2019 2:06:31 PM Phase: A

Location - 20190509140629.hvc
Circuit ID: demo Location: hvi
Sub-Station: NA Building: High Bay Project: TD Demo

Cable - 20190509140629.hvc
Cable Rating: 80kV Conductor: Copper Insulation: Oil

Add Your Company's Logo to Report

The image shows two windows from the E-Link software. The left window is the main interface with a 'RUN TEST' button. The 'Options...' dialog box is open, showing the 'Reports' tab. The 'Title' field contains 'Test Demonstration' and the 'Logo file' field is empty. The right window is a 'Print Preview' of a report titled 'Test Demonstration'. The report includes a table with test results and various parameters.

Options...

Folders Upgrade/Export Communications Languages Reports

Test Demonstration

Title

Logo file

Print Preview

Your company logo will appear here

HVI
VLF-34E

Test Demonstration

Test Summary

File Name	Total Steps	Total Cycles	Total Duration	Tan Delta (E-3)	Capacitance (uF)	Resistance (MOhm)
20170621131241.hvc	4	68	00:11:20	0.517	0.250	> 3.276 M
20170621130302.hvc	3	54	00:09:00	0.337	0.251	> 3.276 M
20170425113621.hvc	4	84	00:14:00	8.115	0.552	374.904

Parameters - 20170621131241.hvc

Waveform: Sinewave Frequency: .1 (hz) Fault: Overload
File Name: c:\20170621131241.hvc Phase: A
Date/Time: 6/21/2017 1:19:18 PM

Location - 20170621131241.hvc

Circuit ID: Location:
Sub-Station: Building: Project:

Location - 20170621130302.hvc

Circuit ID: Location:
Sub-Station: Building: Project:

Export to .csv for Advanced Report Generation

The image shows a workflow for exporting test data. On the left is the E-Link software interface with a 'RUN TEST' button. In the center is a Windows File Explorer window showing a list of .hvc files. A callout box points to one of these files with the text: 'Choose one of the saved test data files which are .hvc files'. On the right is a Microsoft Excel spreadsheet with a callout box stating: 'Your test report data has been imported to .csv format, open with Excel or other spreadsheet program'. The Excel spreadsheet contains the following data:

Sequence Profile	File Name	Date/Time	VLF Mode	Waveform	Frequency	Fault	Phase	Location	Circuit ID	Test Name	Facility	Building
	20170621111342	#####	34E	Sinewave	0.1					'20170621111342'		

Step	Cycles	Source	Vo	Duration (Voltage; P	Current; P	Tan Delta	Capacitan	Resistanc	Event
1	1	22	30	22.119	3.232	-	0.233	6.844		
1	2	22	30	22.142	3.529	-	0.254	6.274		
1	3	22	30	22.141	3.53	-	0.254	6.272		
1	4	22	30	22.141	3.529	-	0.254	6.274		
1	5	22	30	22.141	3.53	-	0.254	6.272		
1	6	22	30	22.141	3.529	-	0.254	6.274		
1	7	22	30	22.141	3.529	-	0.254	6.274		
1	8	22	30	22.141	3.53	-	0.254	6.272		

Build Custom Test Profiles

The image shows the E-Link software interface with three main windows illustrating the profile creation process:

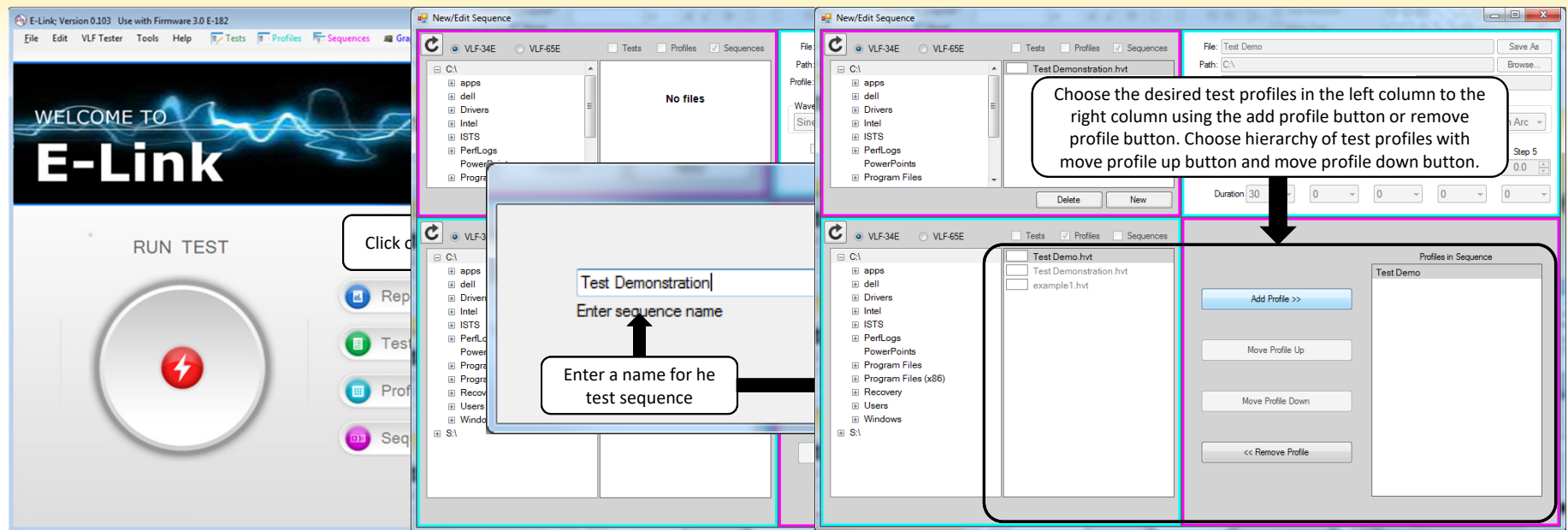
- Left Window:** The main E-Link interface with a "RUN TEST" button and a "WELCOME TO E-Link" banner.
- Middle Window:** The "New/Edit Profile..." dialog. A callout box says "Create a profile name and press ok". The profile name "Test Demonstration" is entered in the text field. Another callout box says "Enter profile name".
- Right Window:** The "Test Demonstration" profile configuration window. Callout boxes provide instructions: "Choose your waveform, frequency, overload reaction, add test voltage and dwell time for up to 5 steps" (pointing to the waveform and voltage step settings) and "Add test location and system identification information here" (pointing to the metadata fields).

Waveform	Frequency	Fault
Sinewave	.10	Overload on Arc

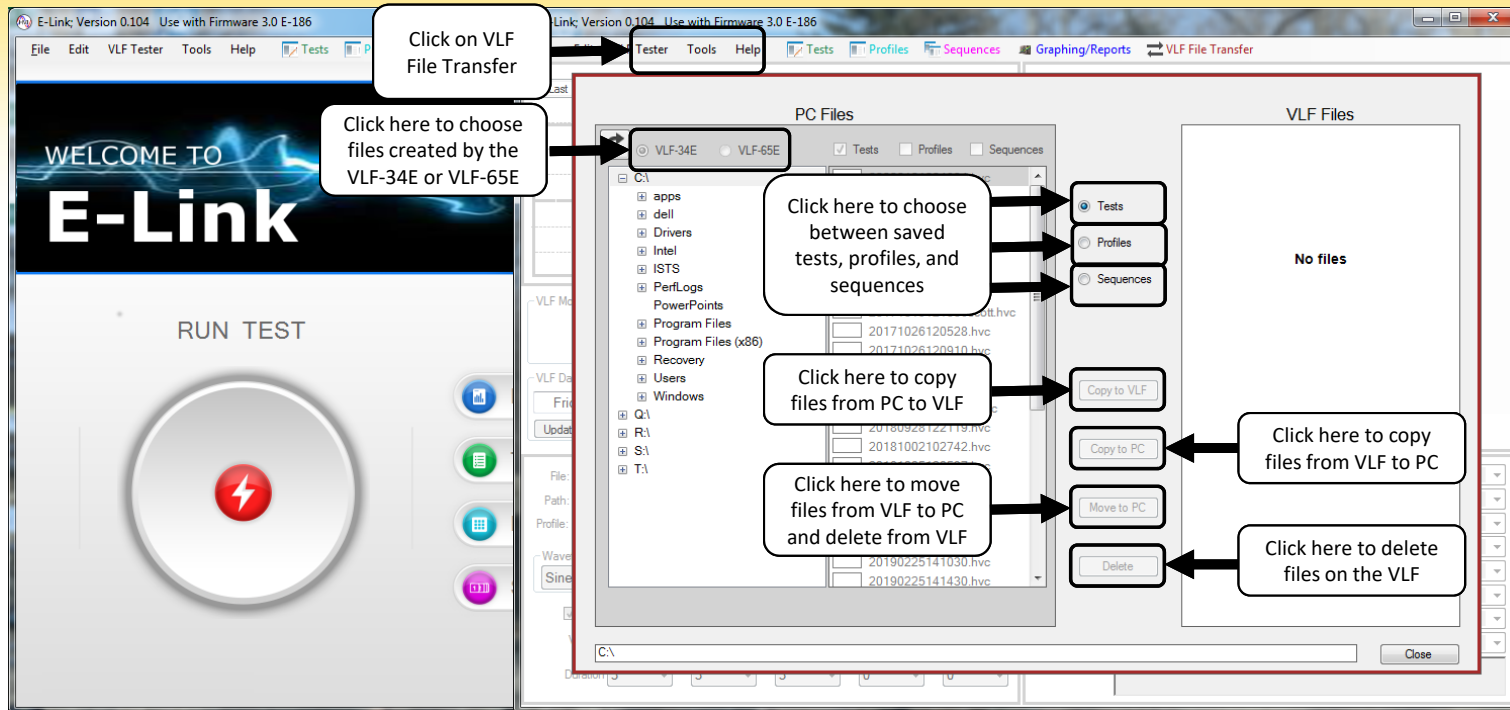
Step	Step 1	Step 2	Step 3	Step 4	Step 5
Voltage	4.0	8.0	12.0	16.0	0.0
Duration	2	3	3	30	0

Field	Value
Circuit ID	
Manufacturer	
Phase	
Insulation	
Location	
Cable Rating	
Project	
Conductor	
Building	
Termination	
Sub-Station	
Environment	
Splices	
Tester	
Length	
Witness	

Build Custom Test Sequences



Wireless File Transfer Between PC and VLF



Simulation Mode

WELCOME TO E-Link

Click run test

Click start twice to confirm action

Choose waveform: + DC, - DC, sinewave or squarewave

Click this box to toggle between RMS and Peak metering, metering in RMS if checked

Choose sim as measurement source

Choose a voltage and dwell time to spend at each voltage

Test In Progress

Graph for analysis of simulated TD, V, I, and time data

Simulated test data

Simulated test data from each step

E-Link Version 0.103 Use with Firmware 3.0 E-182

File Edit VLF Tester Tools Help Tests Profiles Sequences

File Edit VLF Tester Tools Help Tests Profiles Sequences

File Edit VLF Tester Tools Help Tests Profiles Sequences Graphing/Reports VLF File Transfer

Measure Source: VLF VLF/TD TD **Sim**

Signal Strength

File: Path: Profile: Waveform: Sinewave

Step	Voltage	Duration
Step 1	22.0	30
Step 2	0.0	0
Step 3	0.0	0
Step 4	0.0	0
Step 5	0.0	0

Mean: 0.000, Std. Dev: 0.000, Voltage (kV): 0.000, Current (mA): 0.000, Tan Delta (E-3): 0.000, Capacitance (pF): 0.000, Inductance (mH): 0.000

File: 20170624451

Step	Voltage	Duration
Step 1	22.0	30
Step 2	0.0	0
Step 3	0.0	0
Step 4	0.0	0
Step 5	0.0	0

Circuit ID, Phase, Location, Project, Building, Sub-Station, Splices, Length, Comment, Manufacturer, Insulation, Cable Rating, Conductor, Termination, Environment, Tester, Witness

Thank You

from



Thank you for watching, if you have any questions please contact High Voltage, Inc. or your local High Voltage, Inc. sales representative

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